**DLD LAB TASK**

Task 01

|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **INPUTS** | | | | | | | | **OUTPUTS** | | |
| **D0** | **D1** | **D2** | **D3** | **D4** | **D5** | **D6** | **D7** | **Z2** | **Z1** | **Z0** |
| X | X | X | X | X | X | X | 0 | 0 | 0 | 0 |
| X | X | X | X | X | X | 0 | 1 | 0 | 0 | 1 |
| X | X | X | X | X | 0 | 1 | 1 | 0 | 1 | 0 |
| X | X | X | X | 0 | 1 | 1 | 1 | 0 | 1 | 1 |
| X | X | X | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 0 |
| X | X | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 1 |
| X | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 |
| 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |



|  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | **Decimal Inputs** | | | | | | | | | **Outputs** | | | |
| **E1'** | **1'** | **2'** | **3'** | **4'** | **5'** | **6'** | **7'** | **8'** | **9'** | **Q3'** | **Q2'** | **Q1'** | **Q0'** |
| 0 | X | X | X | X | X | X | X | X | 0 | 1 | 0 | 0 | 1 |
| 0 | X | X | X | X | X | X | X | 0 | 1 | 1 | 0 | 0 | 0 |
| 0 | X | X | X | X | X | X | 0 | 1 | 1 | 0 | 1 | 1 | 1 |
| 0 | X | X | X | X | X | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 |
| 0 | X | X | X | X | 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 1 |
| 0 | X | X | X | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 |
| 0 | X | X | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 1 |
| 0 | X | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 0 |
| 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 1 |
| 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 |

Task 02

Circuit Diagram

